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ELEX 7660: Digital System Design

Lab 1

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| Student Name: Taewoo Kim | Student Number: A01284763 | Set: T |

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# Screenshot of Waveforms



Figure 1 – Waveform of bcitid moduleA screenshot of a computer

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Figure 2 - Waveform of decode2 module



Figure 3 – Waveform of decode7 module

# Source code of the module

A screenshot of a computer

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Figure 4 – Source code of the decode2 module

A screenshot of a computer

Description automatically generated

Figure 5 - Source code of the decode7 module

A screenshot of a computer

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Figure 6 - Source code of the bcitid module

# RTL Netlist

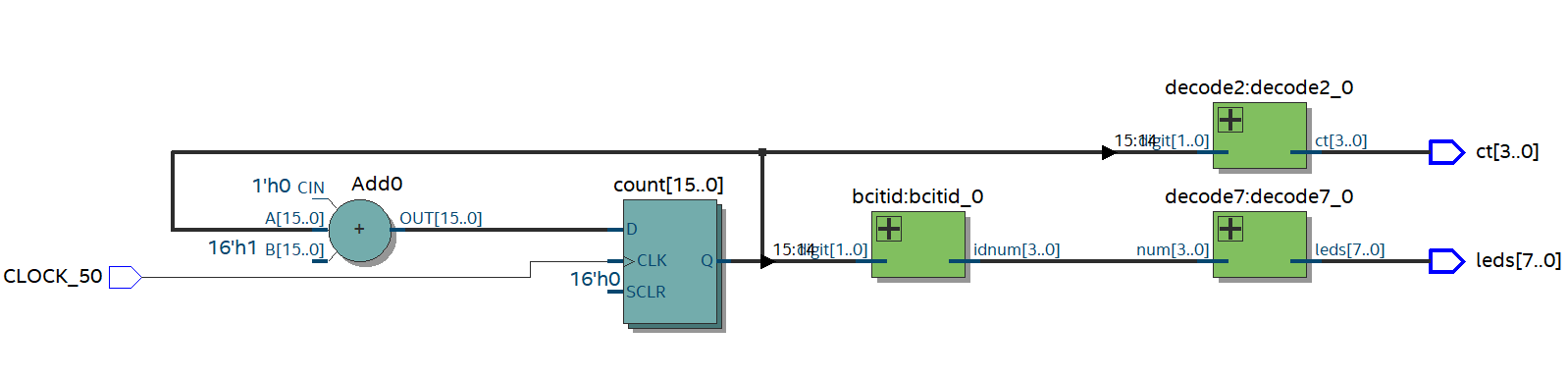


Figure 7 – Final RTL Netlist